IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Original Patent

Patentees: Alex Kalnitsky

Yih-Shung Lin

Patent No.: 5,986,330

Title: ENHANCED PLANARIZATION

TECHNIQUE FOR AN INTEGRATED CIRCUIT

Issued: November 16, 1999

Atty Dk No.: 93-C-032C3 Reissue Application

Applicants: Alex Kalnitsky

Yih-Shung Lin

Serial No.:

Title: ENHANCED PLANARIZATION

TECHNIQUE FOR AN INTEGRATED CIRCUIT

Filing Date: November 16, 2001

Atty Dk No.: 93-C-032RE (1678-42)

FIRST REISSUE APPLICATION DECLARATION BY THE INVENTORS

As a below named inventor, I hereby declare:

My residence, post office address and citizenship are as stated below next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are listed below) of the subject matter that is claimed in patent number 5,986,330 granted November 16, 1999, and for which a reissue patent is sought on the invention entitled:

ENHANCED PLANARIZATION TECHNIQUE FOR AN INTEGRATED CIRCUIT

30.14 - 0				
Ø	is attached hereto.			
	and was amended on date, amendment date, or r when I execute this Declara attorney(s) and/or agents(s	_ as reissue application number (if applicable). reissue application number are not i ration, I authorize the below appointe s) to insert the filing date, amendment r when they become available.	If the filing included ed	
		ntents of the above–identified specific y amendment referred to above.	cation,	
	ge the duty to disclose informitle 37, Code of Federal Reg	mation which is material to patentab gulations, Section 1.56.	ility as	
•	eve the original patent to be w	wholly or partly inoperative or invalid	, for the	

□ by reason of a defective specification or drawing.

oxdot by reason of the patentee claiming less than he had the right to claim in the patent.

by reason of other errors.

the specification of which

Errors upon which reissue is based are described as follows:

I believe that claims 1, 8, and 15 include limitations that overly narrow our invention. For example, our invention does not require "deposition of an interlevel dielectric." Therefore, we have added new, broader method claims 23 - 38.

I believe that claim 22 includes limitations that overly narrow our invention. For example, our invention does not require "an interlevel dielectric." Therefore, we have added new, broader semiconductor-structure claims 39-48.

All errors corrected in this reissue application arose without any deceptive intention on my part.

As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: all attorneys associated with Customer Number 30431.

Correspondence Address: Direct all communications about the application to:

Lisa K. Jorgenson STMicroelectronics, Inc. 1310 Electronics Drive Carrollton, Texas 75006-5039 Phone: (972) 466-6000

Fax: (972) 466-7044

I do not know and do not believe that the claimed invention was ever known or used in the United States of America before my invention thereof.

I do not know and do not believe that the claimed invention was ever patented or described in any printed publication in any country before my invention thereof.

I do not know and do not believe that the claimed invention was ever patented or made the subject of an inventor's certificate issued prior to the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns.

I do not know and do not believe that the claimed invention was ever patented or described in any printed publication in any country more than one year prior to the filing date of the original U.S. application.

I do not know and do not believe that the claimed invention was ever in public use or on sale in the United States of America more than one year prior to the filing date of the original U.S. application.

I hereby claim the benefit of priority, under 35 U.S.C. § 119 and 35 U.S.C. § 120, of any foreign application(s) for patent or inventor's certificate on which priority was claimed in the above-identified issued patent.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine and imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon, or any patent to which this declaration is directed.

Alex Kalnitsky	
Full Name of Inventor	Citizenship
Residence	
Post Office Address (if different from I	Residence)
Inventor's Signature	Date

Yih-Shung Lin			
Full Name of Inventor	Citizenship		
Residence			
Post Office Address (if different from Residence)			
Inventor's Signature	Date		

CERTIFICATE UNDER 37 C.F.R. § 3.73(b)

TMicroelectronics, Inc., a corporaton (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, government agency, etc.) ertifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of ither: A. [X] An assignment from inventor of the patent application identified above. The assignment was recorded in the Patent and Trademark Office at ReelFrame (copy enclosed).	Applicants: Reissue Applicat		Alex Kalnitsky and Yih-Shung Lin		
TMicroelectronics, Inc			ion No.: Filed: November 16, 2001		
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